

FIG. 1 (PRIOR ART)

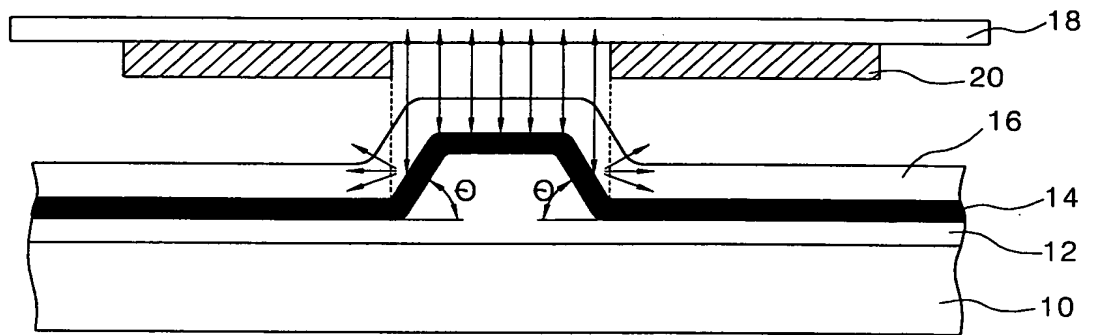
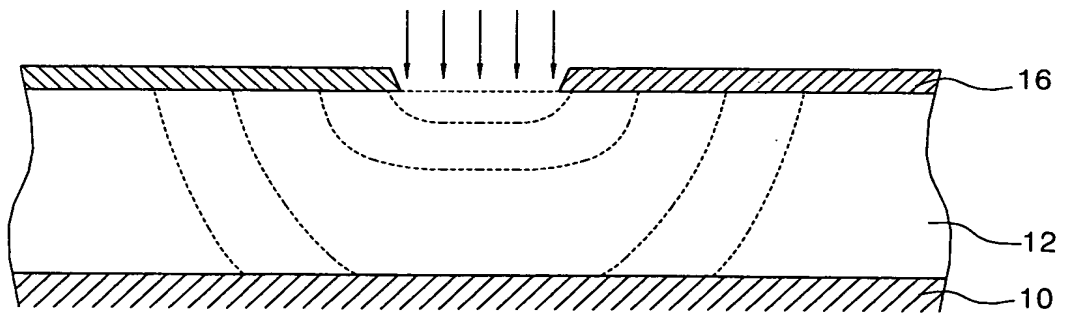


FIG. 2 (PRIOR ART)



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FIG. 3(PRIOR ART)

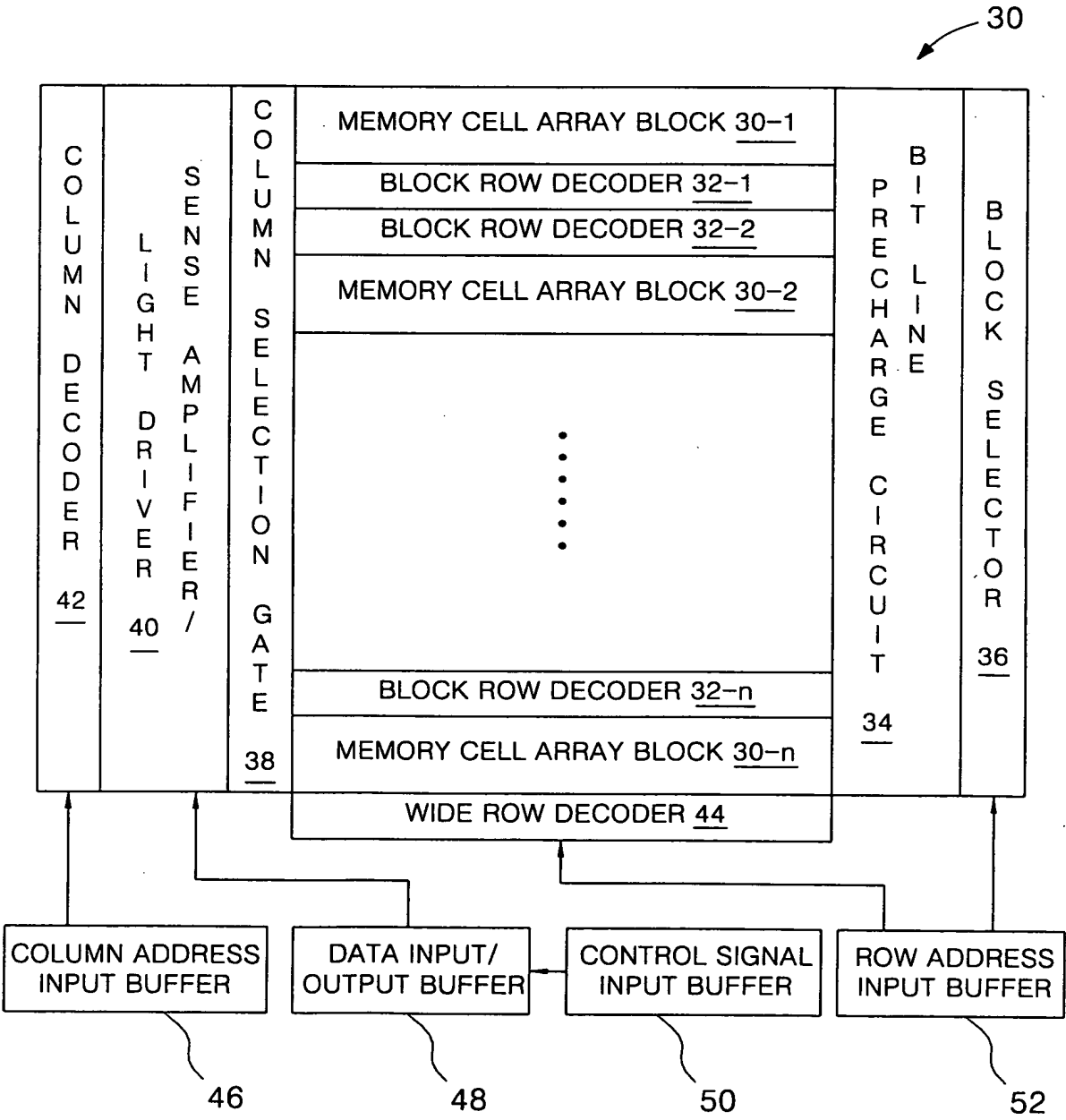
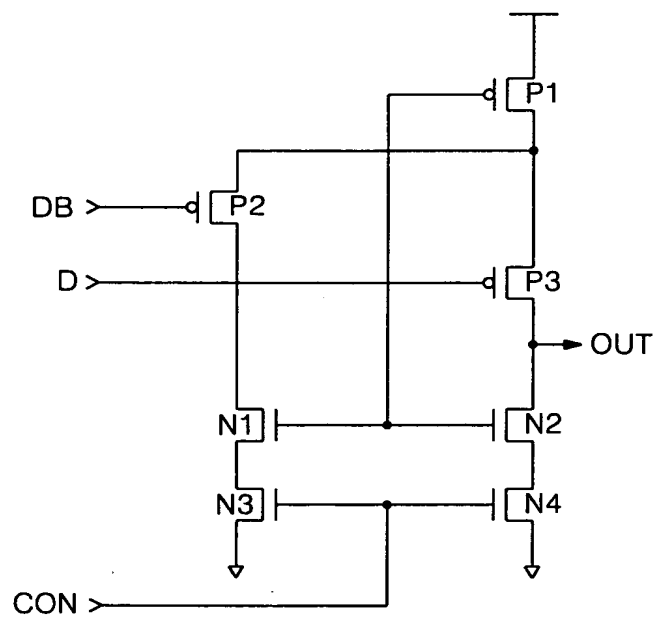
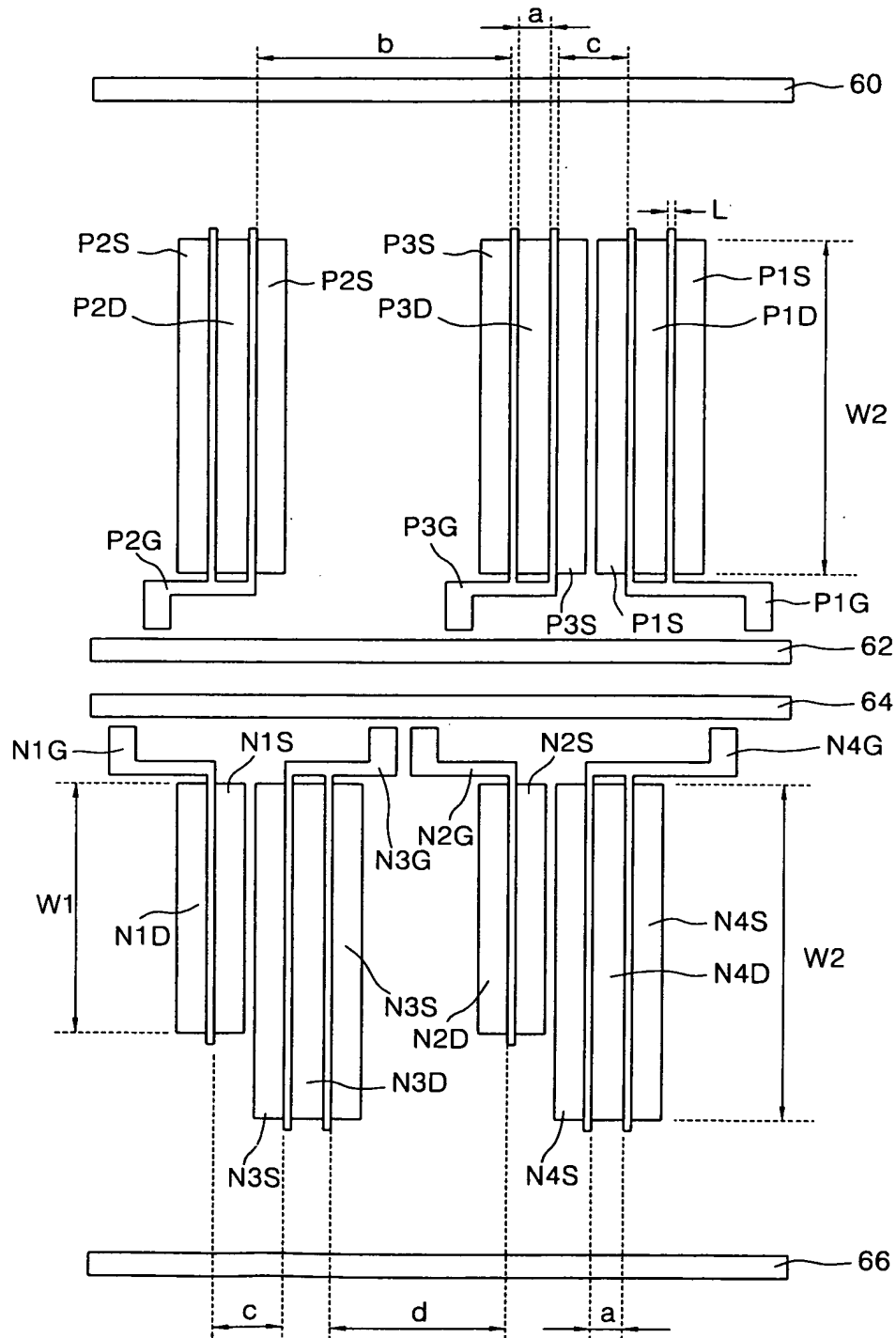


FIG. 4(PRIOR ART)



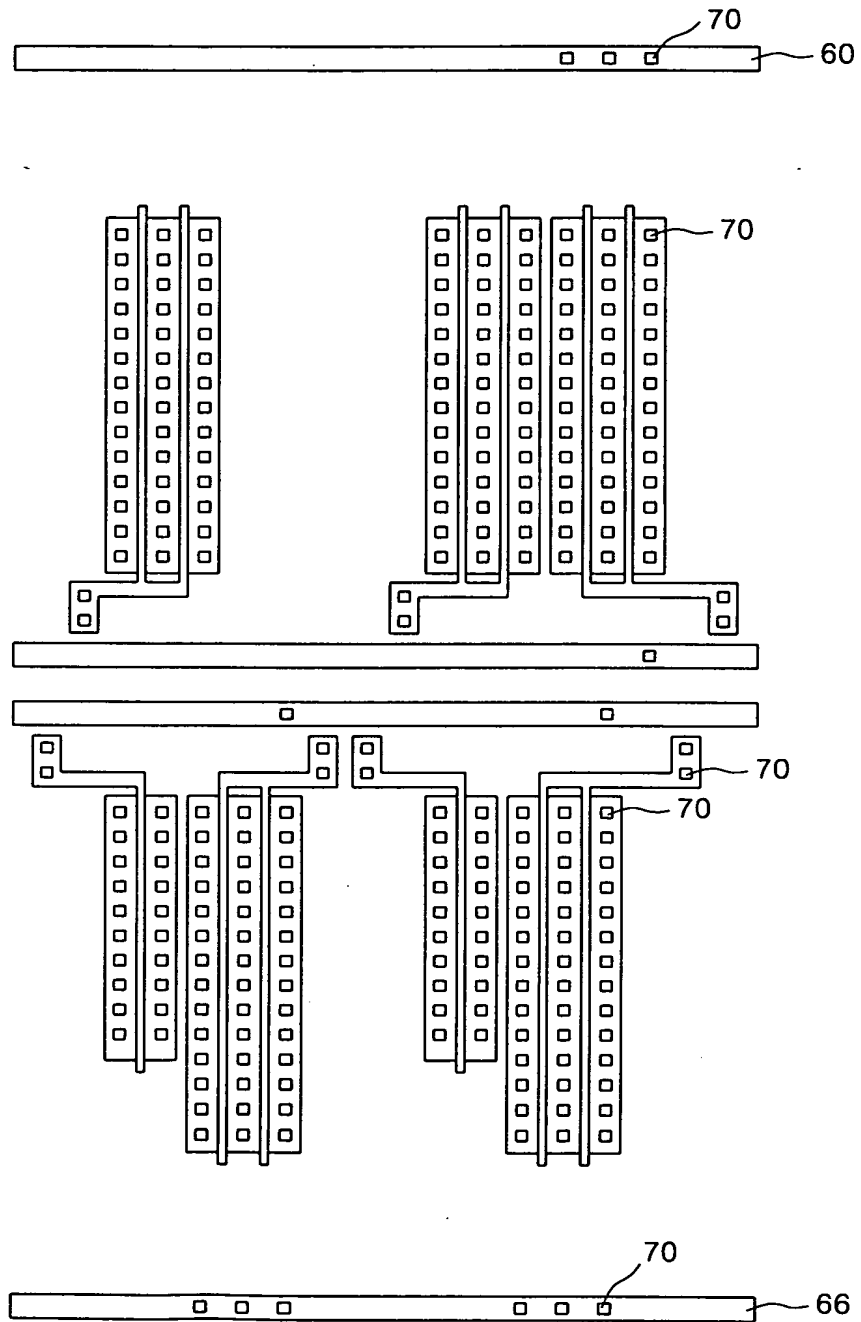
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FIG. 5(PRIOR ART)



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FIG. 6(PRIOR ART)



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FIG. 7(PRIOR ART)

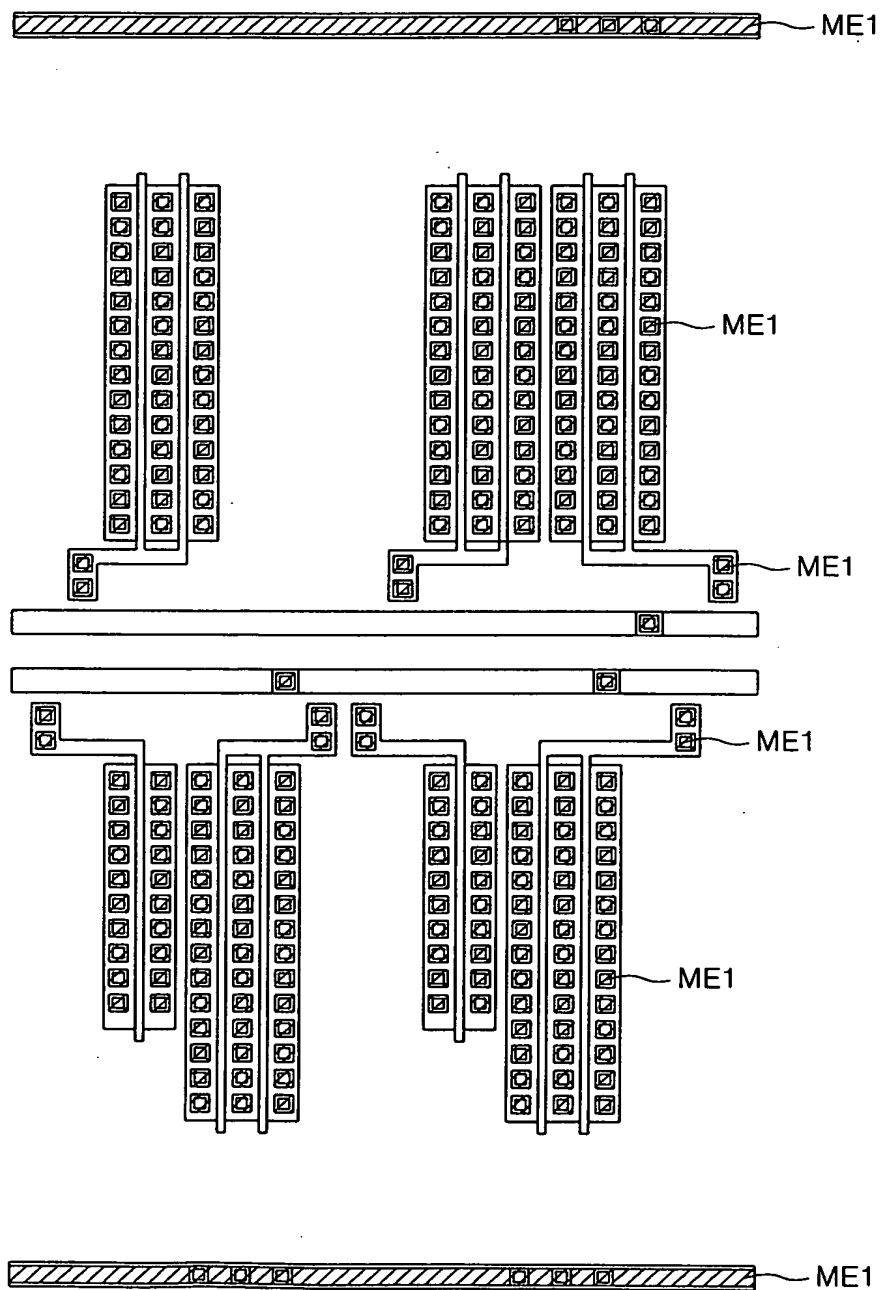


Figure 1 is a schematic diagram of a semiconductor device. It includes a top view and a cross-sectional view. The top view shows a central region with a grid of elements (72) and a surrounding region (ME1). The cross-sectional view shows the device structure with a substrate (72) and a layer (ME1).

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FIG. 9(PRIOR ART)

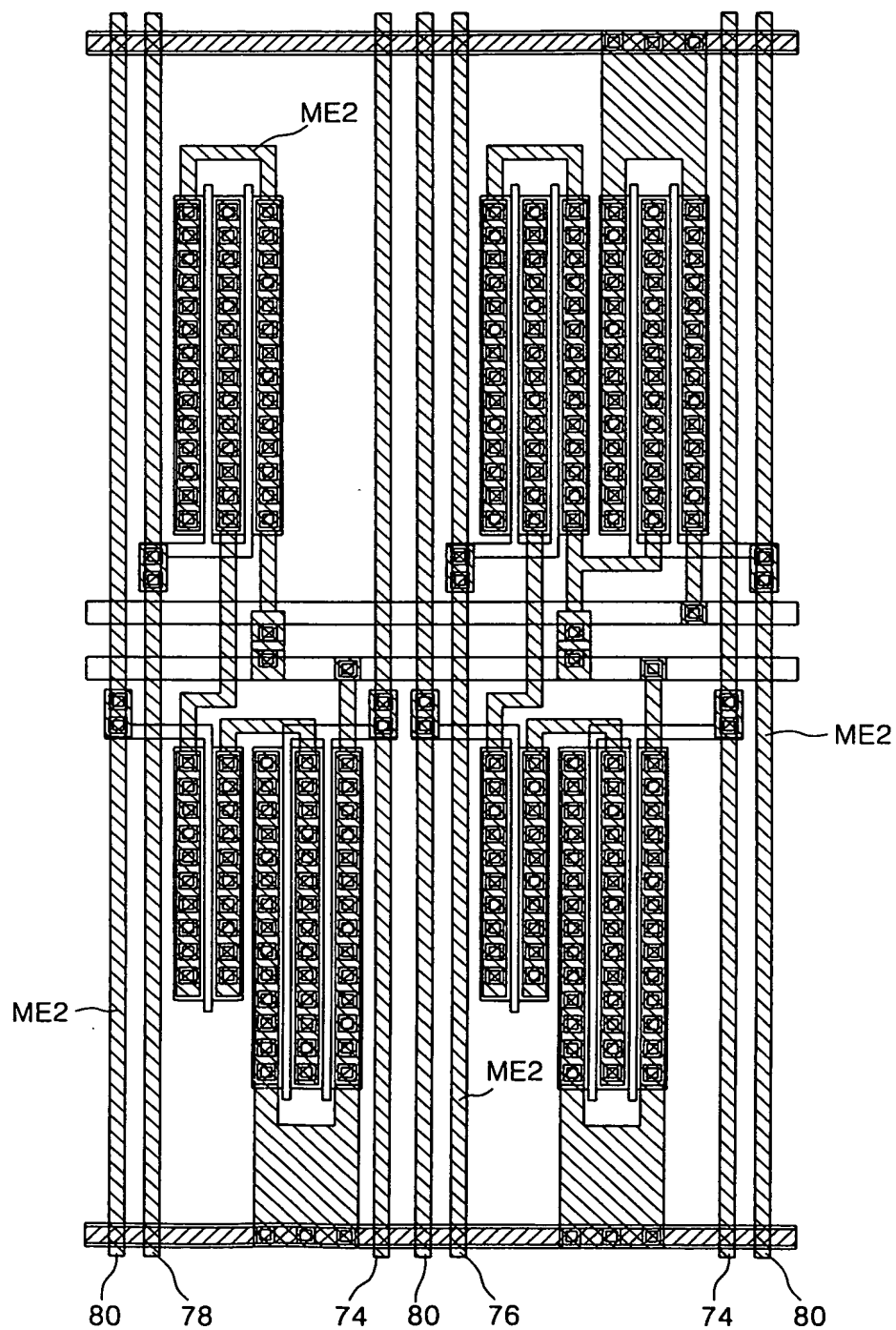


FIG. 10(PRIOR ART)

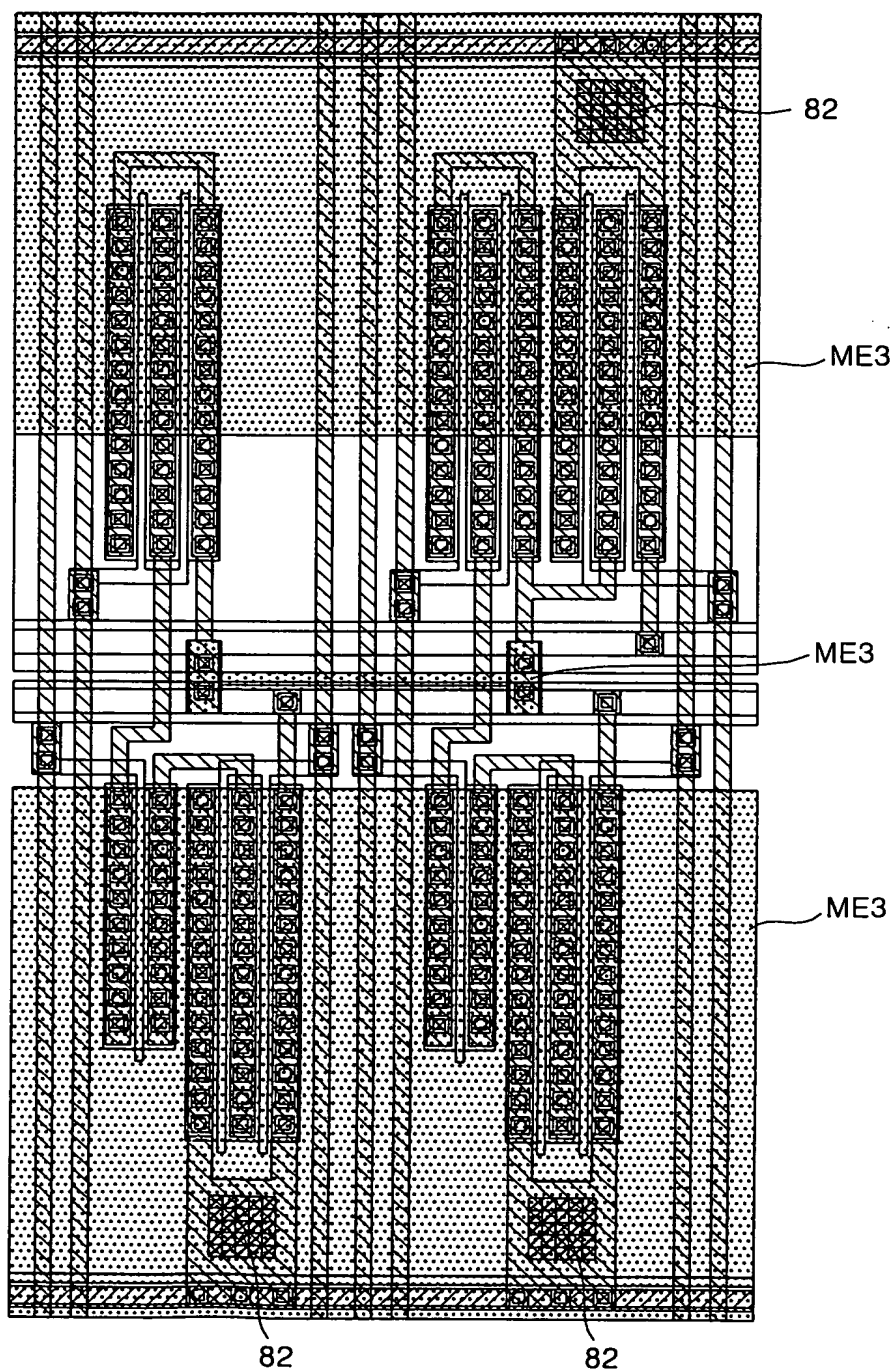


FIG. 11

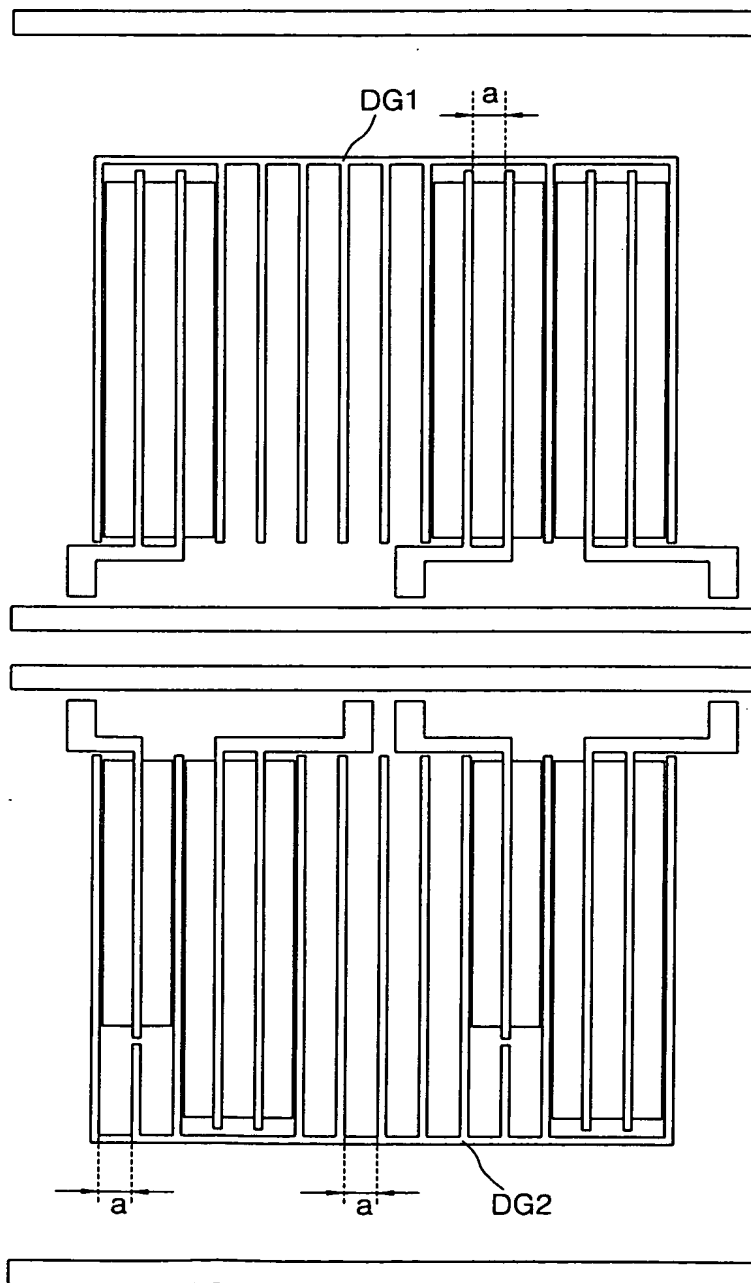


FIG. 12

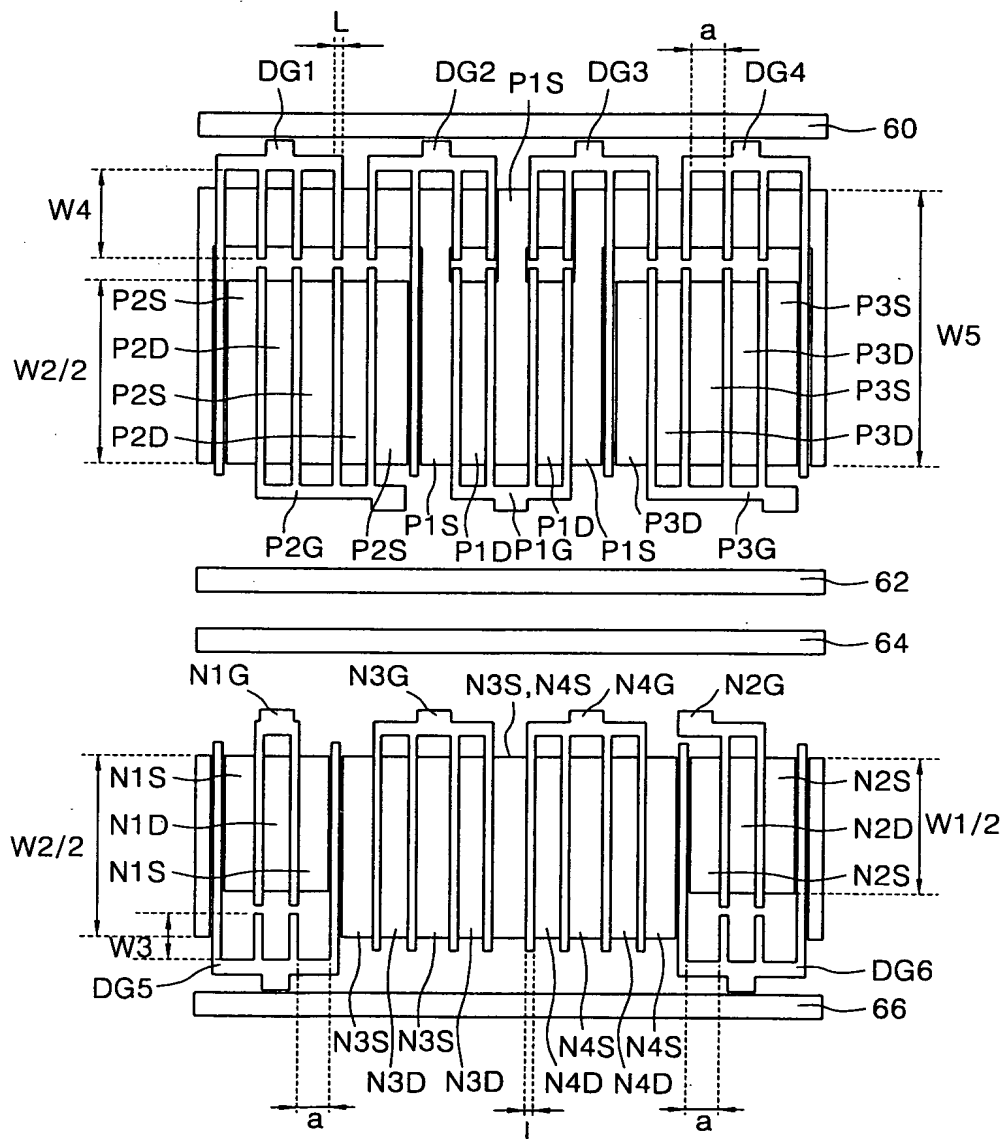


FIG. 13

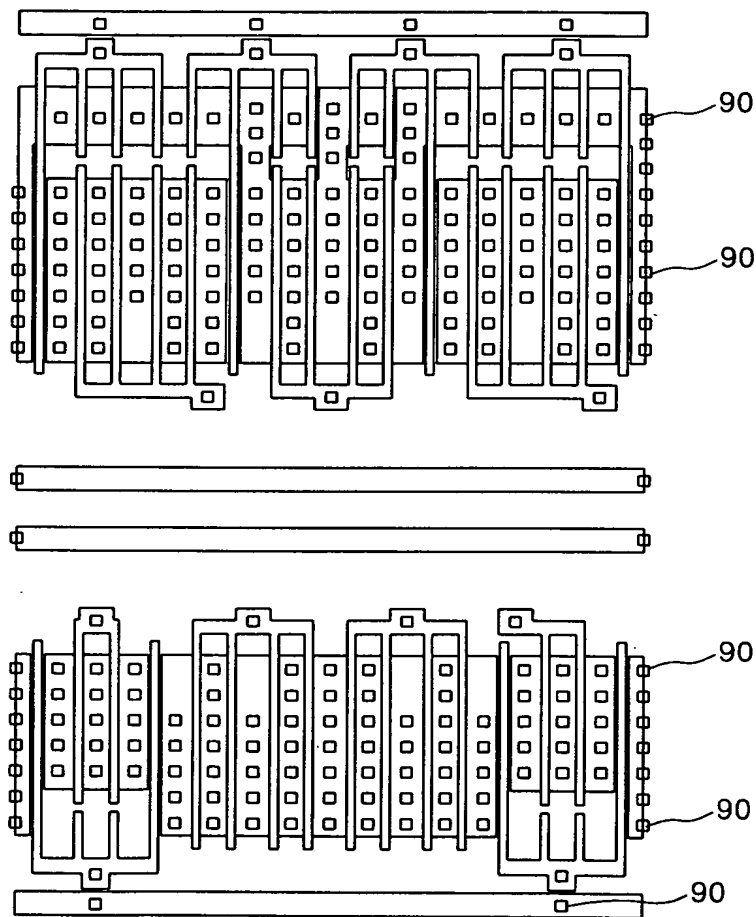


FIG. 14

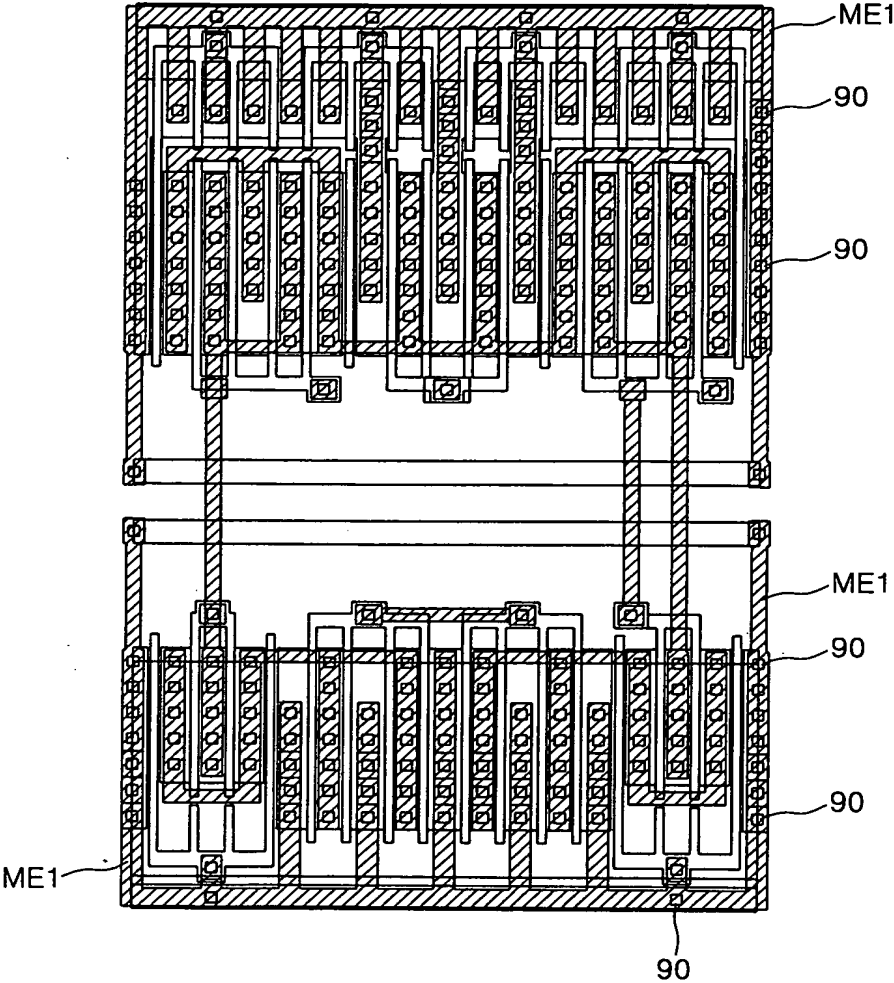
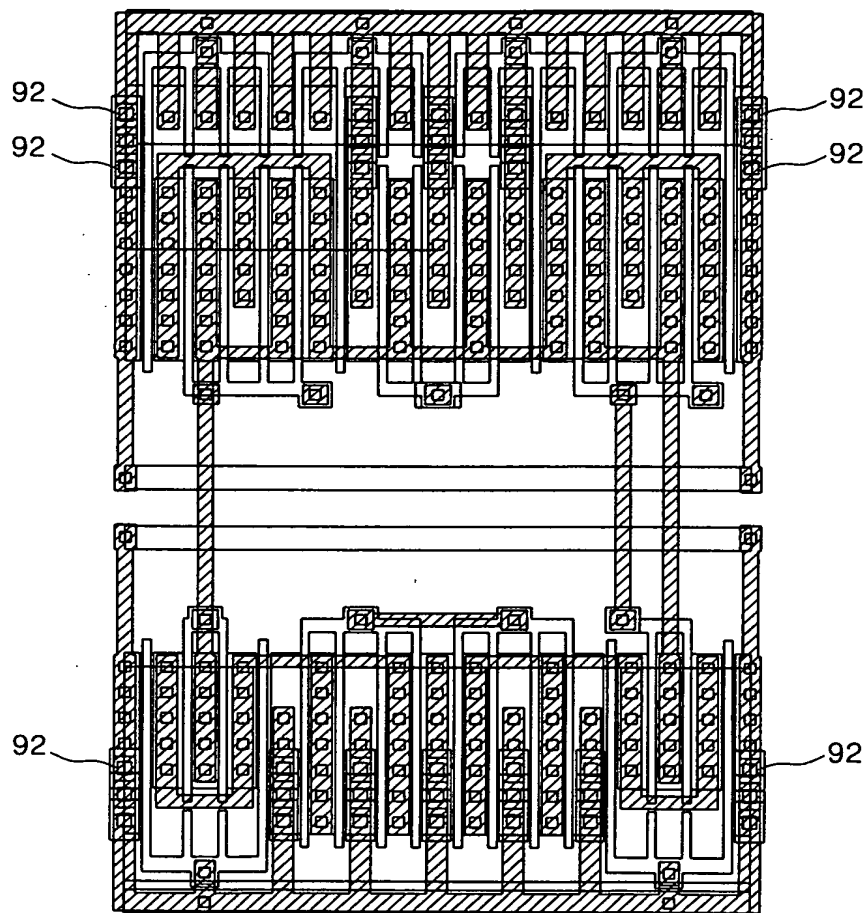


FIG. 15



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FIG. 16

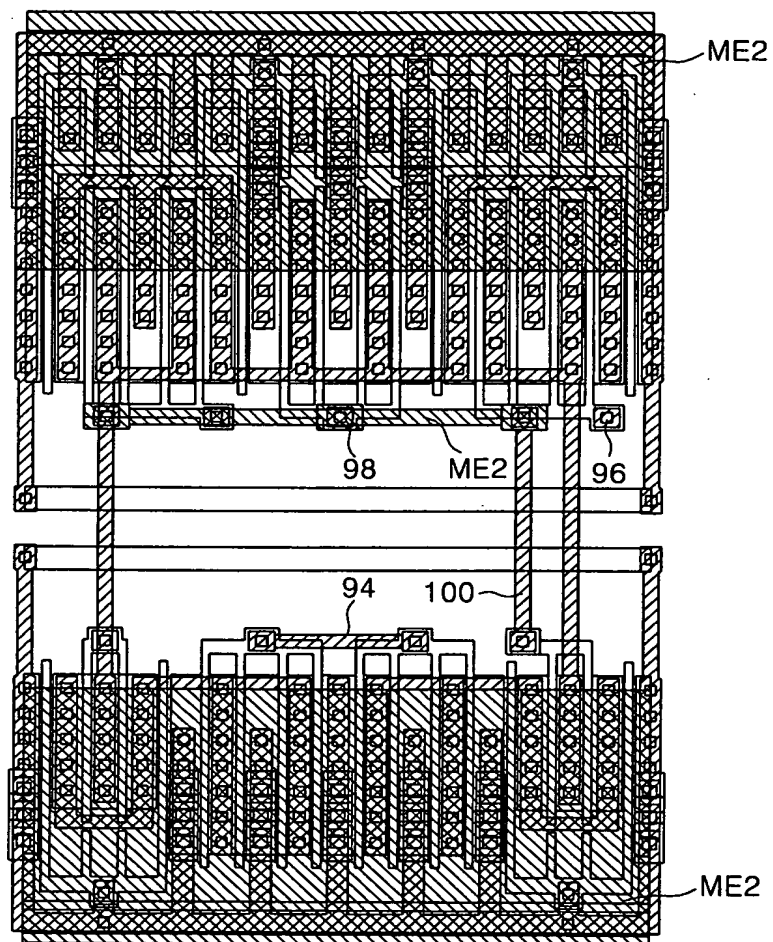
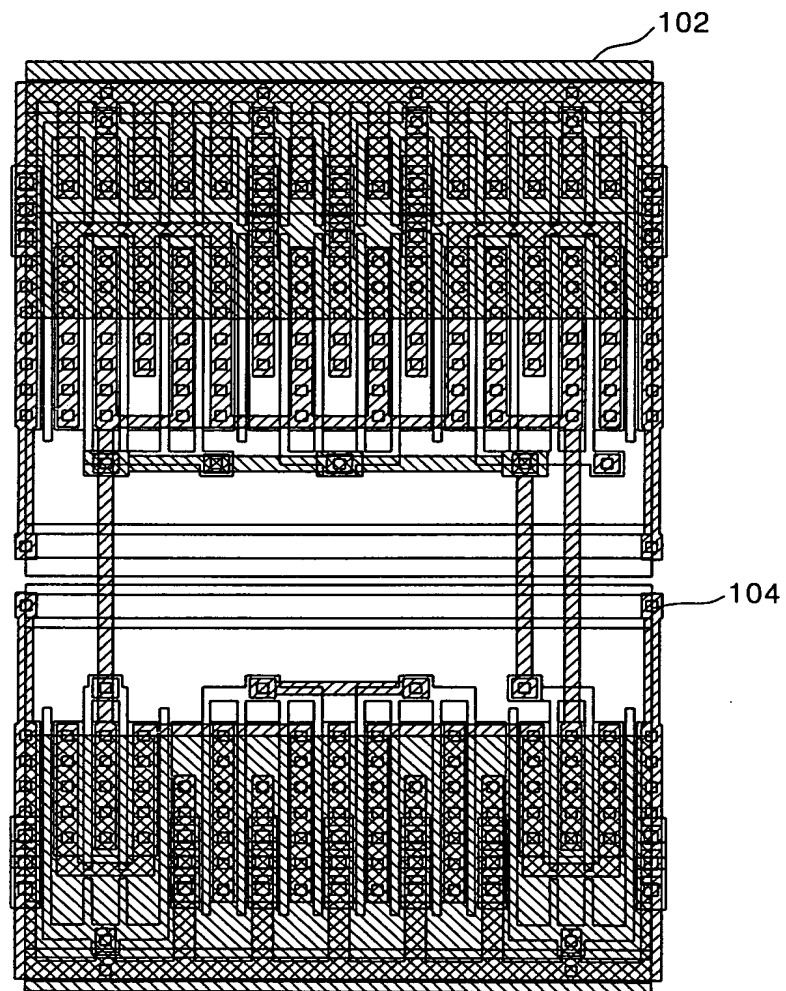


FIG. 17



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FIG. 18

